## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: PARK, Hyun-Ju		) ) Group Art Unit: 2825
Serial N	o.: 10/589,595	) Group Art Omit: 2823
Filed:	August 15, 2006	) ) Examiner: ROSSOSHEK,
For:	CHIP DESIGN VERIFICATION APPARATUS AND DATA COMMUNICATION METHOD	Yelena
	FOR THE SAME	) Confirmation No. 2680

## OK TO ENTER: /H.R./ 06/18/2009

## REPLY TO FINAL OFFICE ACTION UNDER 37 C.F.R. § 1.116, WITH AMENDMENT

Via EFS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Applicant respectfully requests entry of the following amendment and remarks contained herein in response to the Final Office Action dated February 5, 2009. Applicant respectfully submits that the amendment and remarks contained herein place the instant application in condition for allowance.

Amendments to the claims are reflected in the listing of claims, which begins on page 2 of this paper; and

Remarks begin on page 24 of this paper.